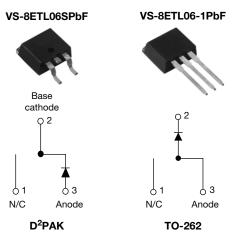


Vishay High Power Products

Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 8 A FRED Pt[®]



PRODUCT SUMMARY				
V _F (typical)	0.96 V			
I _{F(AV)}	8 A			
V _R	600 V			

FEATURES

- Benchmark ultralow forward voltage drop
- Hyperfast recovery time
- · Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD ac-to-dc power supplies.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V _{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 160 °C	8		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	175	А	
Peak repetitive forward current	I _{FM}		16		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage V _F	V	I _F = 8 A	-	0.96	1.05	V	
	I _F = 8 A, T _J = 150 °C	-	0.81	0.86			
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.05	5		
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	100	μA	
Junction capacitance	CT	V _R = 600 V	-	17	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8		8.0	-	nH	



RoHS

COMPLIANT HALOGEN

FREE



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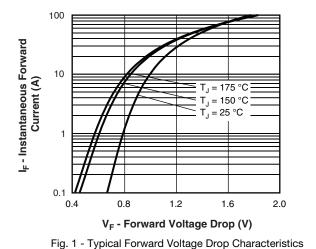
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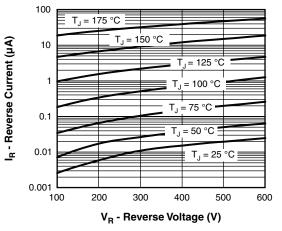
DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	60	100	
		$I_F = 8 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	150	250	
		T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 390 V	-	170	-	ns
		T _J = 125 °C		-	250	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	15	-	A
		T _J = 125 °C		-	20	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	1.3	-	μC
	Vrr	T _J = 125 °C		_	2.6	-	

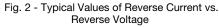
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	1.4	2	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK	8ETL06S			
Marking device		Case style TO-262	8ETL06-1			



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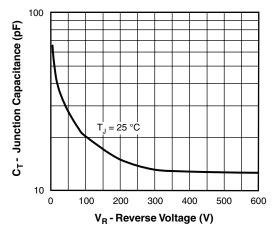


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

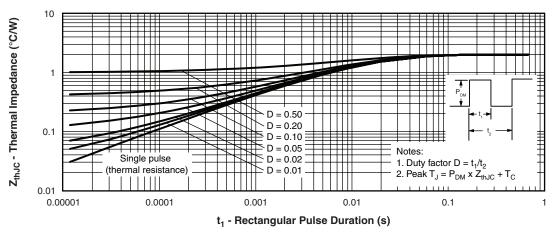


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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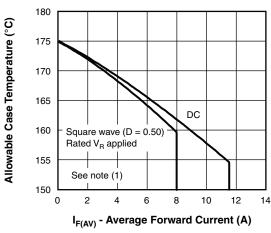


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

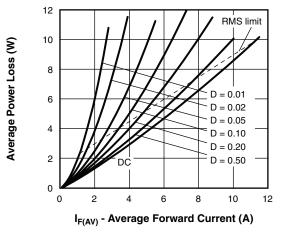


Fig. 6 - Forward Power Loss Characteristics

Note

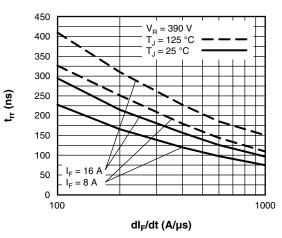
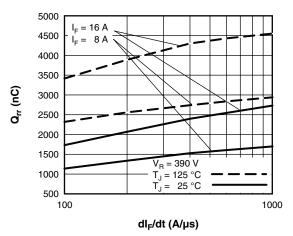


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt







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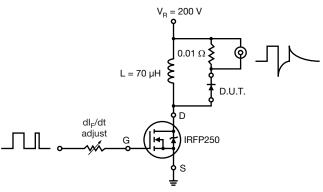
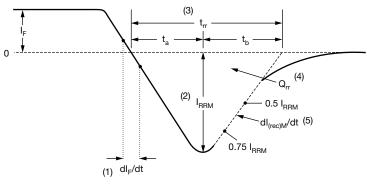


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dI_F/dt - rate of change of current through zero crossing

(4) ${\rm Q}_{\rm rr}$ - area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(2) $\mathbf{I}_{\mathsf{RRM}}$ - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) dl_{(rec)M}/dt - peak rate of change of current during $t_{\rm b}$ portion of $t_{\rm rr}$

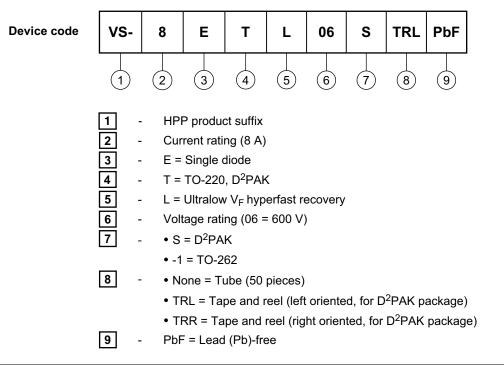
Fig. 10 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			



Vishay

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